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Kristin R. Hayes (Depositor's name)  
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 October 18, 2005 (Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/625,962	07/24/2003	Peter Poeschmueller	INTECH 3.0-097	1620

**TITLE OF INVENTION:** ARRAY TRANSISTOR AMPLIFICATION METHOD AND APPARATUS FOR DYNAMIC RANDOM ACCESS MEMORY

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1400	\$300	\$1700	10/31/2005
EXAMINER		ART UNIT	CLASS-SUBCLASS		
MAL SON LUU		2827	365-208000		

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Infineon Technologies AG

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Munich, Germany

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☐ a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27.

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## FACSIMILE COVER SHEET

To: Commissioner for Patents	Total Pages Sent: 3
Mail Stop Issue Fee	(including cover sheet)
Facsimile Number: (571) 273-2885	Transmission Date: October 18, 2005

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Poechmueller Docket No.: INTECH 3.0-097  
(2003 P 50758 US)

Serial No: 10/625,962 Art Unit: 2827

Date Filed: July 24, 2003

Title: Array Transistor Amplification Method and Apparatus for Dynamic Random Access Memory

## CERTIFICATION OF FACSIMILE TRANSMISSION

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- Certification of Facsimile Transmission (1 page)
- Part B – Fee Transmittal (2 duplicate pages)

Respectfully submitted,

Kristin R. Hayes  
Legal Assistant

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